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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/902,907	07/10/2001	Brian D. Possley	42390P6643C2	1505

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EXAMINER

FARAHANI, DANA

ART UNIT	PAPER NUMBER
2814	

DATE MAILED: 01/13/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/902,907	POSSLEY, BRIAN D.
	Examiner Dana Farahani	Art Unit 2814

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 29 October 2002.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-26 is/are pending in the application.

4a) Of the above claim(s) 1-12 and 21-26 is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 13-20 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) 8	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 13-15, 18 and 19 are rejected under 35 U.S.C. 102(b) as being anticipated by Tran et al., hereinafter Tran (U.S. 5,780,883), previously cited.

Tran discloses in figure 7 a method of fabricating an integrated circuit chip comprising processing a semiconductor substrate to form a gate array architecture of transistors in the substrate, the gate array architecture comprising a plurality of N-type diffusion regions 133 and P-type diffusion regions 134, as shown in the figure; the diffusion regions having partially overlying polysilicon landing sites to form N-type and P-type transistors; wherein the regions are relatively-sized to form two distinct transistor sizes, smaller N- and P-type transistors and larger N- and P-type transistors (see column5, lines 66 and 67; and column 6, lines 1-4).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 16 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tran.

Tran discloses the claimed invention except for expressly disclosing the exact order of transistor sizes.

However, Tran teaches at column 3, lines 18-25, that size of transistors relative to the rows, minimize the area and maximize the speed. Therefore, it would have been within the level of ordinary skill in the art to choose the appropriate ratio between the larger and smaller transistors (or rows), as Tran teaches, in order to minimize the area and maximize the speed.

5. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Tran as applied to claim 19 above, and further in view of Farwell (U.S. 5,576,645), previously cited.

Tran discloses the claimed invention, as discussed above, except for expressly disclosing clock buffers.

Farwell discloses in figures 1 and 3 clock signals in order to control the operation of the transistors (see figure 2A-H). Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to use clock buffers in order to control the operation of the flip-flop.

Response to Arguments

6. Applicant's arguments filed on 10/29/02 have been fully considered but they are not persuasive.

Applicant argues that in Tran a polysilicon landing does not form N and P transistors. However, it is noted that the purpose of diffusion regions in Tran reference is to make P and N transistors with polysilicon sites (gates). See column 4, lines 16-19, the explanation in regard to figure 3A. Also, note that this characteristic of figure 3A of Tran is also disclosed in the embodiment in figure 7, which was used in the rejections of the previous Office Action.

In regard to applicant's argument that the examiner has provided no motivation for combining Tran and Farwell, the examiner notes that there is a motivation, provided in the last part of the last sentence of the paragraph above the Response to Arguments section (this section).

Conclusion

7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

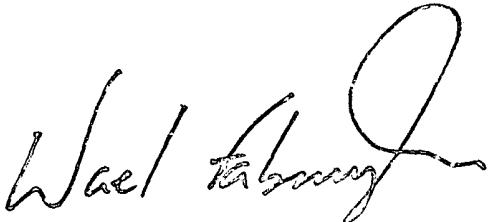
Art Unit: 2814

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dana Farahani whose telephone number is (703)305-1914. The examiner can normally be reached on M-F 8:00AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (703)308-4918. The fax phone numbers for the organization where this application or proceeding is assigned are (703)872-9318 for regular communications and (703)872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)308-0956.

Dana Farahani
January 8, 2003


Wael Fahmy
SUPERVISORY PRIMARY EXAMINER
TECHNOLOGY CENTER 2000